

ABSTRACT OF THE DISCLOSURE

A thin film transistor array substrate includes an insulating substrate; a thin film transistor
5 arranged in a part of a pixel area on the insulating substrate; an auxiliary capacitor having an auxiliary capacitor electrode arranged in the pixel area and an opposite electrode facing the auxiliary capacitor electrode via an insulating layer, the
10 opposite electrode being located in the same layer as source and drain electrodes of the thin film transistor; and a pixel electrode formed in the pixel area. The opposite electrode is divided into two or more sections, and each of the sections is
15 electrically connected to the pixel electrode via a contact hole.